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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yeo, et al.

Docket No.:

TSM03-0553

Serial No.:

10/667,871

Art Unit:

2812

Filed:

September 22, 2003

Examiner:

Jennifer Kennedy

For:

Resistor with Reduced Leakage

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Respectfully submitted,

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

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2812

Title:

Resistor with Reduced Leakage

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08A & 08B, which may be considered material to the examination of the above-identified application.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

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Respectfully submitted,

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PTO/SB/08a (08-03)

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Substitute for form 1449AVPTO Complete if Known 10/667,871 **Application Number** INFORMATION DISCLOSURE Filing Date 9/22/2003 STATEMENT BY APPLICANT First Named Inventor Yeo, et al. Art Unit 2812 (Use as many sheets as necessary) **Examiner Name** Jennifer Kennedy Sheet 2 TSM03-0553 Attorney Docket Number

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ^{2 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	1	US-4,069,094	01-17-1978	Shaw, et al.		
	2	US-4,497,683	02-05-1985	Celler, et al.		
	3	US-5,155,571	10-13-1992	Wang, et al.		
	4	US-5,378,919	01-03-1995	Ochiai		
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	8	US-5,955,766	09-21-1999	lbi, et al.		
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	14	US-6,518,610 B2	02-11-2003	Yang, et al.		
	15	US-6,521,952 B1	02-18-2003	Ker, et al.		
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Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages	۔		
Initials*	No.1	Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	or Relevant Figures Appear	'		
	20	EP 0683522 A2	11-22-1995	International Business Machines Corporation				
	21	EP 0828296 A2	03-11-1998	International Business Machines Corporation				

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Substitute for form 1449A/PTO			· · · · · · · · · · · · · · · · · · ·	Complete if Known		
Gui	DSultate for form 1449/VF 10			Application Number	10/667,871	
	NFORMATION I	DISCI	LOSURE	Filing Date	9/22/2003	
l s	STATEMENT BY	APF	LICANT	First Named Inventor	Yeo, et al.	
	44			Art Unit	2812	
	(Use as many sheets	as neces	ssary)	Examiner Name	Jennifer Kennedy	
Sheet	2	of	2	Attorney Docket Number	TSM03-0553	

U.S. PATENT DOCUMENTS						
Examiner Cite		Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where	
Examiner Initials*	No.1	Number - Kind Code ^{2 (if known)}	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear	
**************************************	22	US-6,617,643 B1	09-09-2003	Goodwin-Johansson		
	23	US-6,633,070 B2	10-14-2003	Miura, et al.		
	24	US-6,657,276 B1	12-02-2003	Karlsson, et al.		
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	26	US-6,724,019 B2	04-20-2004	Oda, et al.		
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	28	US-6,762,448 B1	07-13-2004	Lin, et al.		
	29	US-6,794,764 B1	09-21-2004	Kamal, et al.		
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	32	US-2002/0074598 A1	06-20-2002	Doyle, et al.		
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	34	US-2004/0173815 A1	09-09-2004	Yeo, et al.		
	35	US-2005/0029601 A1	02-10-2005	Chen, et al.		

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²		
	36	LEITZ, C. W., et al., "Hole mobility enhancements in strained Si/Si _{1-y} Ge _y p-type metal-oxide-semiconductor field-effect transistors grown on relaxed Si _{1-x} Ge _x (x <y) 17,="" 2001,="" 25.<="" 4246-4248,="" 79,="" applied="" december="" letters,="" number="" physics="" pp.="" substrates,"="" td="" virtual="" volume=""><td></td></y)>			
	37	LEITZ, C. W., et al., "Channel Engineering of SiGe-Based Heterostructures for High Mobility MOSFETs," Mat. Res. Soc. Symp. Proc., 2002, pp. 113-118, Vol. 686.			
	38	LIU, K. C., et al., "A Novel Sidewall Strained-Si Channel nMOSFET," IEDM, 1999, pp. 63-66.			

Examiner	Date
Signature	Considered

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